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PTO/SB/05 (2/98)

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UTILITY PATENT APPLICATION TRANSMITTAL

Attorney Docket No. 4015-814

First Inventor or Application Identifier

Paul W. Dent

Title

COMMUNICATION SYSTEM AND METHOD WITH
ORTHOGONAL BLOCK ENCODING

Express Mail Label No.

EL634166646US

(Only for new nonprovisional applications under 37 CFR § 1.53(b))

APPLICATION ELEMENTS

See MPEP chapter 600 concerning utility patent application contents.

ADDRESS TO:

Assistant Commissioner for Patents
Box Patent Application
Washington, DC 20231

1. ☒ *Fee Transmittal Form (e.g., PTO/SB/17)
(Submit an original and a duplicate for fee processing)
2. ☒ Specification [Total Pages 47]
(preferred arrangement set forth below)
 - Descriptive title to the Invention
 - Cross References to Related Applications
 - Statement Regarding Fed sponsored R & D
 - Reference to Microfiche Appendix
 - Background of the Invention
 - Brief Summary of the Invention
 - Brief Description of the Drawings (if filed)
 - Detailed Description
 - Claim(s)
 - Abstract of the Disclosure
3. ☒ Drawing(s) (35 U.S.C. 113) [Total Sheets 8]
4. ☒ Oath or Declaration [Total Pages 3]
 - a. ☐ Newly executed (original or copy)
 - b. ☒ Copy from a prior application (37 C.F.R. § 1.63(d))
(for continuation/divisional with Box 17 completed)
[Note Box 5 below]
 - i. ☐ DELETION OF INVENTOR(S)
Signed statement attached deleting
inventor(s) named in the prior application,
see 37 C.F.R. §§ 1.63(d)(2) and 1.33(b).
5. ☐ Incorporation By Reference (useable if Box 4b is checked)
The entire disclosure of the prior application, from which a
copy of the oath or declaration is supplied under Box 4b,
is considered as being part of the disclosure of the
accompanying application and is hereby incorporated by
reference therein.

6. ☐ Microfiche Computer Program (Appendix)
7. Nucleotide and/or Amino Acid Sequence Submission
(if applicable, all necessary)
 - a. ☐ Computer Readable Copy
 - b. ☐ Paper Copy (identical to computer copy)
 - c. ☐ Statement verifying identity of above copies

ACCOMPANYING APPLICATION PARTS

8. ☐ Assignment Papers (cover sheet & document(s))
9. ☐ 37 C.F.R. § 3.73(b) Statement ☒ Power of Attorney
(when there is an assignee)
10. ☐ English Translation Document (if applicable)
11. ☐ Information Disclosure Statement (IDS)/PTO-1449 ☐ Copies of IDS
Citations
12. ☒ Preliminary Amendment
13. ☒ Return Receipt Postcard (MPEP 503)
(Should be specifically itemized)
14. ☐ *Small Entity ☐ Statement filed in prior application,
Statement(s) Status still proper and desired
15. ☐ Certified Copy of Priority Document(s)
(if foreign priority is claimed)
16. ☒ Other: Express Mail Certification
17. ☒ Other: Associate Power of Attorney

*NOTE FOR ITEMS 1 & 14: IN ORDER TO BE
ENTITLED TO PAY SMALL ENTITY FEES, A SMALL
ENTITY STATEMENT IS REQUIRED (37 C.F.R.
§ 1.27), EXCEPT IF ONE FILED IN A PRIOR
APPLICATION IS RELIED UPON (37 C.F.R. § 1.28).

17. If a CONTINUING APPLICATION, check appropriate box and supply the requisite information below and in a preliminary statement:

☒ Continuation ☐ Divisional ☐ Continuation-in-part (CIP) of prior application No: 08 / 898.392
Prior application information: Examiner: A. Boakye Group/Art Unit: 2733

18. CORRESPONDENCE ADDRESS

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Name (Print/Type)

David E. Bennett

Registration No. (Attorney/Agent)

32,194

Signature

David E. Bennett

Date

November 7, 2000

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FEE TRANSMITTAL

Patent fees are subject to annual revision on October 1

These are the fees effective December 29, 1999.

Small Entity payments must be supported by a small entity statement.

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See 37 C.F.R. §§ 1.27 and 1.28.

TOTAL AMOUNT OF PAYMENT (\$) 710.00

Complete if Known

Application Number	TBA
Filing Date	TBA
First Named Inventor	Paul W. Dent
Examiner Name	TBA
Group Art Unit	TBA
Attorney Docket No.	4015-814

METHOD OF PAYMENT (check one)

- 1.
- ☒
- The Commission is hereby authorized to charge indicated fees and credit any over payments to:

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Deposit Account Name Coats & Bennett, P.L.L.C.

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- Charge Any Additional Fee Required Under 37 CFR §§1.16 and 1.17
- ☐
- Charge the Issue Fee Set in 37 CFR §1.18 at the Mailing of the Notice of Allowance

- 2.
- ☒
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FEE CALCULATION**1. BASIC FILING FEE**

Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid
101	690	201	345	Utility filing fee	710.00
106	310	206	155	Design filing fee	
107	480	207	240	Plant filing fee	
108	760	208	380	Reissue filing fee	
114	150	214	75	Provisional filing fee	
SUBTOTAL (1)					(\$) 710.00

2. EXTRA CLAIM FEES

	Extra Claims	Fee from below	Fee Paid
Total Claims	8	-20** =	X = -0-
Independent Claims	2	-3** =	X = -0-
Multiple Dependent Claims		X =	

** or number previously paid, if greater; For Reissues, see below

Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid
103	18	203	9	Claims in excess of 20	
102	78	202	39	Independent claims in excess of 3	
104	260	204	130	Multiple dependent claim	
109	78	209	39	**Reissue independent claims over original patent	
110	18	210	9	**Reissue claims in excess of 20 and over original patent	
SUBTOTAL (2)					(\$) -0-

FEE CALCULATION (continued)**3. ADDITIONAL FEES**

Large Fee Code	Entity Fee (\$)	Small Fee Code	Entity Fee (\$)	Fee Description	Fee Paid
105	130	205	65	Surcharge - late filing fee or oath	
127	50	227	25	Surcharge - late provisional filing fee or cover sheet	
139	130	139	130	Non-English specification	
147	2,520	147	2,520	For filing a request for reexamination	
112	920*	112	920*	Requesting publication of SIR prior to Examiner action	
113	1,840*	113	1,840*	Requesting publication of SIR after Examination action	
115	110	215	55	Extension for reply within first month	
116	380	216	190	Extension for reply within second month	
117	870	217	435	Extension for reply within third month	
118	1,360	218	680	Extension for reply within fourth month	
128	1,850	228	925	Extension for reply within fifth month	
119	300	219	150	Notice of Appeal	
120	300	220	150	Filing a brief in support of an appeal	
121	260	221	130	Request for oral hearing	
138	1,510	138	1,510	Petition to institute a public use proceeding	
140	110	240	55	Petition to revive - unavoidable	
141	1,210	241	605	Petition to revive - unintentional	
142	1,210	242	605	Utility issue fee (or reissue)	
143	430	243	215	Design issue fee	
144	580	244	290	Plant issue fee	
122	130	122	130	Petitions to the Commissioner	
123	50	123	50	Petitions related to provisional applications	
126	240	126	240	Submission of Information Disclosure Stmt.	
581	40	581	40	Recording each patent assignment per property (times number of properties)	
146	760	246	380	Filing a submission after final rejection (37 CFR 1.129(a))	
149	760	249	380	For each additional invention to be examined (37 CFR 1.129(b))	
Other fee (specify)					
Other fee (specify)					
*Reduced by Basic Filing Fee Paid					
SUBTOTAL (3)					(\$) -0-

SUBMITTED BY

Typed or Printed Name David E. Bennett

Signature

Date

November 7, 2000

Complete (if applicable)

Reg. Number 32,194

Deposit Account User ID 18-1167

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Paul W. Dent

Serial No. _____

Filed: _____

For: **COMMUNICATION SYSTEM AND
METHOD WITH ORTHOGONAL
BLOCK ENCODING**

Attorney's Docket No. P-4015.814

**Express Mail Certification
Label No.EL634166646US**

Raleigh, North Carolina
November 7, 2000

Assistant Commissioner for Patents
BOX PATENT APPLICATION
Washington, D.C. 20231

Sir:

EXPRESS MAIL CERTIFICATE LABEL NO. EL634166646US

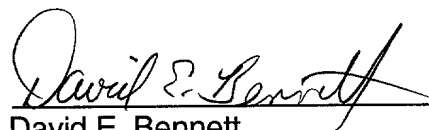
DATE MAILED: November 7, 2000

I hereby certify that the enclosed Utility Patent Application Transmittal, Fee Transmittal Form (2 copies), specification and claims, drawings (1 set of 8 sheets), Declaration and Power Of Attorney, Associate Power of Attorney, Preliminary Amendment, and our Check # 2991 in the amount of \$710.00, are being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 C.F.R. §1.10 on the date indicated above and is addressed to Assistant Commissioner for Patents, BOX PATENT APPLICATION, Washington, D.C. 20231.

Respectfully submitted,

COATS & BENNETT, P.L.L.C.

By:



David E. Bennett

Registration No. 32,194

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of)
)
Paul W. Dent)
)
Serial No.: TBD)
) A. Boakye
Filed: TBD) Examiner
) Group Art Unit 2733
For: **COMMUNICATION AND METHOD**)
WITH ORTHOGONAL BLOCK)
ENCODING)
)
Attorney Docket No.: P-4015.814)

Raleigh, North Carolina
November 7, 2000

Assistant Commissioner for Patents
Box Patent Application
Washington, D.C. 20231

PRELIMINARY AMENDMENT

Dear Sir:

If any fees for the Preliminary Amendment are required, the Applicant's submits that this be considered a Petition therefore, and the Commissioner is hereby authorized to charge Deposit Account 18-1167.

Please amend the application as follows:

IN THE SPECIFICATION:

Under the title on page 1, please insert the following: --This is a continuation of U.S. patent application serial number 08/898,392, filed July 22, 1997.--.

IN THE CLAIMS

Please cancel claims 1-64 without prejudice.

Please add new claims 65-72 as follows:

65. A method for communicating between a plurality of transmitters and a receiver, said method comprising:

orthogonally encoding information signals at each transmitter, said orthogonally encoding comprising:

coding information signals at said transmitter to produce corresponding information

blocks containing coded information symbols;

repeating each information block a predetermined number of times according to a

predetermined spectrum-spreading factor;

orthogonally encoding said repeated information blocks by applying a modification

factor to each repeat of said information blocks, the sequence of modification

factors applied to successive repeats of said information blocks forming one

of a set of mutually orthogonal coding sequences; and

transmitting said orthogonally encoded information signals from said plurality of

transmitters.

66. The method according to claim 65 further comprising decoding said orthogonally encoded information signals by employing different orthogonal codes respectively for separating the information blocks corresponding to different information signals into corresponding separate channels.

67. The method of claim 66 wherein decoding said orthogonally encoded information signals comprises removing orthogonal encoding from said encoded information blocks and adding corresponding ones of said information symbols in repeated information blocks.

68. The method according to claim 67 wherein removing said orthogonal encoding from said encoded information blocks comprises forming a summed signal for each symbol within the repeated information blocks and processing the summed signal with an equalizer.

69. The method of claim 65 wherein applying a modification factor to each repeat of said information blocks comprises imposing a sequence of phase changes onto said repeated information blocks.

70. The method of claim 65 wherein applying a modification factor to each repeat of said information blocks comprises imposing a sign change on said repeated information blocks.

71. A receiver to receive a transmitted signal comprising repeated information blocks that have been orthogonally encoded, said receiver comprising:

a receiver circuit to receive an orthogonally encoded information signal comprising at

least one repeated information block; and

a decoder to decode said orthogonally encoded repeated information block.

72. The received of claim 71 wherein said decoder comprises:
- an orthogonal code remover from removing the orthogonal encoding from said repeated information blocks; and
- an adder for adding corresponding ones of said symbols in said repeated information blocks after the orthogonal encoding is removed by said orthogonal code remover to form a summed signal for each symbol within said repeated information blocks.

Respectfully submitted,

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Express Mail Label No.: EL634166646US

Date Mailed: November 7, 2000

UNITED STATES PATENT APPLICATION FOR GRANT OF LETTERS PATENT



Paul W. Dent
INVENTOR

00407590520260

COMMUNICATION SYSTEM AND METHOD WITH ORTHOGONAL BLOCK ENCODING

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Background of the Invention

The present invention relates generally to a communication system and method with transmittal signal encoding and particularly to an orthogonal communication method
5 employing orthogonal encoding.

Deliberate bandwidth expansion, by redundant coding, is presently employed because of the advantage it confers on the parameters of performance. However, this advantage can be lost if the communication channel suffers from delayed echoes, time-dispersion or multipath effects.

10 Code Division Multiple Access, or CDMA, is a known technique often proposed to artificially widen transmission bandwidths. CDMA is an extension of well known redundant coding techniques such as the technique of repeat transmissions with majority vote at the receiver to combine signal repeats. In some applications of CDMA, also known as Direct Sequence Spread Spectrum, a mixture of simple repeats, or "dumb spreading,"
15 and error correction coding, or "intelligent spreading," is employed to achieve desired bandwidth widening ratio.

It is known in the prior art that it is advantageous to use less intelligent coding and to substitute an element of dumb spreading in such a way that different signals become orthogonal to one another and then do not interfere with each other. For example, if one
20 signal after a suitable amount of intelligent error correction coding yields a coded bit stream $a_1, a_2, a_3, a_4, \dots$ and a second signal yields a coded bit stream $b_1, b_2, b_3, b_4, \dots$

Then the first signal is transmitted using additional four-times repeat coding as $a_1, a_1, -a_1, -a_1, a_2, a_2, -a_2, -a_2, a_3, a_3, -a_3, -a_3, a_4, a_4, -a_4, -a_4, \dots$ while the second signal is transmitted

with four times repeat coding as $b_1, -b_1, -b_1, b_1, b_2, -b_2, -b_2, b_2, b_3, -b_3, -b_3, b_3, b_4, -b_4, -b_4, b_4, \dots$,
then a comparison of the sign pattern of the repeat coding $++-- ++-- ++-- ++-- \dots$ for the
first signal and the sign pattern of the repeat coding $+--+ +--+ +--+ +--+ \dots$ for the
second signal, shows that these differ in sign in exactly half the positions while agreeing in
5 the other half. Thus, upon combining the repeats with the proper signs for enhancing one
signal, the contribution from the interfering signal completely cancels, and vice versa.
These signals are known as "mutually orthogonal."

The U. S. digital cellular IS95 system specifies mutual orthogonality for
transmissions from cellular base stations to mobile phones, using 64-fold repeat coding
10 with one of 64 sign patterns selected from a set of 64 mutually orthogonal Walsh-
Hadamard codes. The IS95 system uses non-orthogonal transmission in the direction from
mobile phone to cellular base stations, using instead intelligent error correction coding
comprising convolutional encoding concatenated with orthogonal Walsh-Hadamard block
coding. In the mobile-to-base direction, the orthogonality between different Walsh-
15 Hadamard codes is used to discriminate between different 6-bit symbols transmitted from
the same mobile phone, while in the base-to-mobile direction, the Walsh-Hadamard codes
are used to discriminate between symbols transmitted to different mobile phones.

A disadvantage of the IS95 system of non-orthogonal transmissions in the mobile-
to-base direction is that these signals interfere with one another if the power of the mobile
20 transmitter is not strictly controlled as a function of distance from the base station such
that signals from different mobile phones are received at more or less the same power
level. However, the need for strict power control is alleviated when practicing the
invention disclosed in U. S. Patent No. 5,151,919 issued to Dent on September 29, 1992,

entitled *CDMA Subtractive Demodulation*. In U. S. Patent No. 5,218,619 issued to Dent on June 8, 1993, entitled *CDMA Subtractive Demodulation*, already decoded signals are subtracted more than once to improve interference subtraction. U. S. Patent No.

5,353,352 issued to Dent and Bottomley on October 4, 1994, entitled *Multiple Coding for*

5 *Radio Communications*, describes optimum spread spectrum access codes, equivalent to the sign patterns discussed above, when orthogonal signaling is employed within one transmission with non-orthogonality between different transmissions, such as used in an IS95 uplink in the mobile-to-base direction. The disclosures of the above referenced patents are hereby incorporated by reference here in their entirety.

10 The reason for the difference between IS95 uplink, (mobile-to-base), and IS95 downlink, (base-to-mobile), transmission schemes is that maintaining orthogonality between different transmissions requires that they be accurately aligned in time, when the prior art communication schemes are used. If, in the above example, the first and second signals are aligned with one another with a one place shift they are shown as follows:

15 ++--++--++--++--
 +--+++--++--++--+

The two exemplary sign patterns given above are seen now to differ only at the beginning and the end of symbol blocks, thus severely compromising orthogonality.

20 In the downlink, or base-to-mobile, direction all signals originate at the same base station and thus time alignment can be assured. When signals in the uplink or mobile-to-base direction originate at different mobile phones that lie at different distances from the base station, it is much more difficult to achieve time alignment of the signals received at the base station.

Demodulation of Digitally Modulated Signals, the disclosures of which are hereby incorporated by reference herein. The need for a guard time between time slots reduces the bandwidth capability of the systems while use of an equalizer does not eliminate all potential multipath propagation problems.

5 A need therefore still exists for a system and method that constructs and communicates signals that remain largely orthogonal to each other even when delayed by different amounts of time due, for example, to multipath propagation phenomenon.

Summary of the Invention

10

The deficiencies of the prior art described above are alleviated when practicing a communication system and method with orthogonal encoding in accordance with the present invention. The communication system and method of the present invention provides for repetitively transmitting encoded signals with mutually orthogonally encoded
15 repeated blocks of symbols, the symbols in the repeated blocks representing coded information. Decoding of the orthogonally encoded repeated blocks of symbols of the transmitted encoded signal is provided.

In accordance with one aspect of the invention, a communication system is described with orthogonal block encoding and comprises a plurality of transmitters each
20 with means for repetitively transmitting encoded signals with mutually orthogonally encoded repeated blocks of symbols respectively representing samples of an informational source signal produced at the transmitter. A receiver is provided for receiving the encoded transmitted signals including means for decoding the orthogonally encoded repeated

blocks of symbols of the transmitted encoded signals received from all the plurality of transmitters. The decoding is provided by employing different ones of a plurality of orthogonal codes respectively associated with different ones of the plurality of transmitters for separating the received encoded signals into corresponding separate channels.

5 In yet another aspect of the invention, the communication system each of the plurality of transmitters may repeat each bit of information produced by a digital source encoder a preselected first number of times to successively produce groups of repeated bits. A sign change is selectively imposed on the repeated bits of each of a second number of successive groups of repeated bits in accordance with an orthogonal code associated
10 with the transmitter.

Interleaving of the sign changed bits from the second number of groups is then carried out to successively generate a number of blocks equal to said first number containing said second number of symbols each block comprising different coded information bits sharing a common sign change. A modulated signal is transmitted in
15 accordance with the generated blocks with sign changes corresponding to the orthogonal code.

These and other features and advantages of the present invention will become apparent from the following detailed description, the accompanying drawings, and the appended claims.

Brief Description of the Drawings

In the drawings:

Fig. 1 is a simplified functional block diagram of an orthogonal block encoding
5 communication system of the present invention;

Fig. 2 is an illustration of two of the orthogonally block encoded signals received
at the receiver of the system of Fig. 1 which are nonsynchronized by an amount to which
the orthogonal block encoding receiver is entirely insensitive;

Fig. 3 is an illustration like that of Fig. 2 but showing a departure from ideal
10 orthogonality;

Fig. 4 is an illustration like that of Fig. 2 showing the effect of multipath
propagation;

Fig. 5 is a functional block diagram of a transmitter in accordance with the
invention;

15 Fig. 6 is a functional block diagram of an alternative transmitter arrangement in
accordance with the invention;

Fig. 7 is a functional block diagram of a receiver in accordance with the invention;

Fig. 8(a) shows a prior art GSM TDMA burst and format of data bits; and

Fig. 8(b) is an illustration like that of Fig. 8(a) but showing the delay insensitive
20 orthogonal CDMA transmission of data bits in accordance with the invention.

Detailed Description of the Invention

Referring to Fig. 1, an orthogonal block encoding communication system 10 of the present invention is seen to include a plurality of transmitters exemplified by an pair of substantially identical block encoding transmitters 11 and 12 which broadcast information carrying signals S11 and S12 in the form of electro-magnetic waves. Preferably these signals S11 and S12 are digital signals, although the invention contemplates and is usable with analog signals modulated onto a carrier wave. These signals S11 and S12 are received by an orthogonal block encoding receiver 14 which decodes the orthogonally block encoded signals and separates them into separate output channels. A portion of the orthogonally block encoded signal S11 from transmitter 11, as shown with a dotted line, reaches the receiver 14 via an indirect path by reflecting off of a reflective object 13 on the landscape. Because the length of the reflective path is greater than the length of the direct path of the signal S12 the reflected signal S11' arrives at the receiver 14 at a time later than the arrival of directly received signal S12. Accordingly, even if signal S12 is synchronized to arrive at the receiver simultaneously with the arrival of signal S11, it will not be synchronized with the reflected signal S11'.

Referring to Fig. 2, an orthogonal block encoding communication system 10 is shown. The first signal S11, comprises blocks of N information-bearing samples $b_1, b_2, b_3, \dots, b_N$ which are repeated a number of times with inversion indicated by a minus sign or without inversion denoted by a plus sign over each block. Thus, as shown in Fig.2, S11-1, S11-3, and S11-4, the first, third and fourth blocks are not inverted, while the

second block S11-2 is inverted. The inversion/non-inversion pattern for Fig. 2 is therefore represented by the sign pattern $+ - + +$.

The second signal S12 comprises a block of signal samples $a_1, a_2, a_3, \dots, a_N$ which is also repeated with or without an inversion. In the case of the second signal S12, there is
5 no inversion for the first, second and third repeats, but inversion of the fourth repeat, represented by the sign pattern $+++ -$.

It may be verified that the first and second signals' sign patterns $+ - + +$ and $+++ -$ are orthogonal, which means that they agree in as many places as they disagree.

When the first signal S11, and second signal S12, are both transmitted at the same
10 time, linear addition of signal samples occurs in the aether. However, as shown in Fig. 2, the two signals S11 and S12, or signal blocks S11-1 and S12-1, are not necessarily time-aligned. In the example of Fig. 2, the samples a_i and b_i are not aligned and so do not add, while samples a_i and $b_{(i+2)}$ are aligned and do add.

The receiver 14 is connected to receive corresponding signal samples that are
15 repeated in a transmission time T apart. The receiver 14 preferably converts the signal samples into a suitable form, such as numerical, which are stored in a receiver sample memory 15. The receiver 14 processes and combines corresponding signal samples received a time period T apart by reading them out of the memory 15 if they are previously received samples. At the four sample points exemplified in Fig. 2, the sum of
20 the sample values from signals S11 and S12 are respectively

$$a_1 + b_3, a_1 - b_3, a_1 + b_3 \text{ and } -a_1 + b_3.$$

In combining the samples, the receiver 14 uses addition or subtraction according to the sign pattern associated with the signal. In the example of Fig. 2, the sign pattern

+--+ is used to receive the first signal S11. Alternatively, the sign pattern +++- is used to receive the second signal S12.

In receiving the first signal S11 therefore, the receiver 14 forms,

$$+(a_1+b_3) -(a_1-b_3) +(a_1+b_3) +(-a_1+b_3) = 4b_3,$$

5 which illustrates that interference from the samples a_1 and $-a_1$ of the second signal S12 cancel.

Alternatively, the receiver 14 combines the received samples using sign pattern +++- to form the second received signal S12, obtaining,

$$+(a_1+b_3) +(a_1-b_3) +(a_1+b_3) -(-a_1+b_3) = 4a_1,$$

10 showing that interference from the samples b_3 and $-b_3$ of the first signal S11 cancels.

Thus the two signals S11 and S12 appear orthogonal despite having a relative time misalignment of two sample intervals. The same orthogonality will hold for other time misalignments relatively small compared to the block length of N sample intervals.

Departures from ideal orthogonality when practicing the invention occur for some
15 repeated bits, the number of bits for which this occurs being equal to the time misalignment expressed in sample intervals. Thus, as shown in Fig. 3, when the block duration is large compared to the time misalignment, departures from orthogonality affect only a small fraction of the bits. The receiver 14 combines received samples to decode the samples using sign pattern +--+ . For b_1 , the interference from 'a' sample a_3 cancels.

20 However, for decoding b_N , the receiver 14 obtains

$$4b_N -a_2 +a_2' \dots\dots\dots(1)$$

The interference from the 'a' samples to b_N does not cancel completely because a_2' is a sample from the next set of block repeats, and is not necessarily equal to a_2 . When the

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$$4C_0.a_3+4C_1.a_2$$

$$4C_0.a_4+4C_1.a_3$$

...

$$4C_0.a_N+4C_1.a_{(N-1)}$$

5 where a_N means the Nth symbol of the previous block of N symbols. All outputs except the first depend on two transmitted samples.

The output sequence may be processed by an equalizer, such as described in the incorporated references, designed to handle delayed paths of one or more samples delay. Such an equalizer processes all samples correctly except for those at the border between
10 two blocks, such as the first sample given by equation (2) above. Samples at the edge of blocks are handled appropriately by such an equalizer. The degree of approximation is better when the number of combined repeats 'M' is larger than four, such that the first output becomes

$$M.C_0.a_1+(M-3).C_1.a_N-a_N = M.(C_0.a_1+C_1.a_N - C_1.(a_N+3.a_N)/M).....(3)$$

15 where the error $C_1.(a_N+3.a_N)/M$ tends to zero in relation to $C_0.a_1+C_1.a_N$ as M becomes larger. It is possible, however, to effectively model the dependence of the receiver after combining based on three samples, a_1, a_N and a_N in the above example of Fig. 4, and to construct an equalizer that uses this model in decoding a_1 while using a model dependent on only two transmitted samples otherwise. Such an equalizer needs to maintain a larger
20 number of decoding states or "Viterbi states" to resolve the signal dependence on the additional symbols.

In CDMA systems, the receiver 14 of the invention thus includes despreading followed by conventional equalization for multipath propagation. In accordance with the

invention, the receiver 14 may include a Viterbi Maximum Likelihood Sequence estimator form of equalizer or a Decision Feedback Equalizer (DFE), or, alternatively, a suitable RAKE receiver which accounts for multipath propagation in the despreading process. A suitable RAKE receiver is described in U. S. Patent No. 5,305,349 issued to Dent on April 19, 1994 entitled *Quantized Coherent RAKE Receiver*, which is hereby incorporated by reference.

A transmitter 16 in accordance with the invention is preferably constructed as shown in Fig. 5 including a block interleaver 18 which operates on the signal after a final orthogonal spread-spectrum coding operation is performed by circuit 20. The circuit 20 includes a bit repeater 22, a direct sequence orthogonal code generator 24 and a modulo-2 adder 26.

An information source 28 provides information, such as speech or facsimile signals, to a digital source encoder 30 which converts the information into digital form. The output of the digital source encoder 30 is applied to an error correction encoder 32 to render transmissions more tolerant to noise and interference. The output bit stream of the encoder 32 (b_1, b_2, b_3, \dots) is spread by a bit repeater 22 which samples each bit M times where M is the desired spreading factor. By then bitwise adding, the modulo-2 adder 26 bitwise adds to the spread bit stream, a characteristic orthogonal code allocated for the signal and generated by the direct sequence orthogonal code generator 24. A MxN block interleaving operation is performed by the block interleaver 18 on the spread spectrum coded signal on output such that repeated bits are not transmitted adjacently in time but rather separated by a block size of N bits. The block interleaver 18 does not add or delete bits, but alters their order of transmission, for example, by transposing a matrix of NxM

bits. Alternatively, the block interleaver 18 is a helical, diagonal or block-diagonal interleaver rather than a purely block interleaver. The spread spectrum coded block signal is then applied to a radio frequency carrier by means of a modulator 33.

The transmitter 16 of Fig. 5 is formed by adding an interleaver 18, having precise
5 parameters (M,N) adapted to the spread spectrum code produced by the generator 24, to result in a CDMA transmitter according to the invention.

Fig. 6 shows an alternative transmitter 35 in accordance with the invention which includes the information source 28, digital source encoder 30 and error correction
encoder 32.

10 The embodiments shown in both Figs. 5 and 6 can include further interleaving over and beyond the interleaving performed by the interleaver 18, the purpose of the further interleaving being to avoid errors in the same sample block appearing consecutively at the error correction decoder at the receiver 14. Any such additional within-block interleaving is considered to be part of the error correction coding process.

15 The output from the error correction encoder 32 is connected to a block repeater unit 36 which saves a block of N consecutive bits and then repeats the block M times. A block sign generator 37 selectively supplies a sign for each repetitive block. Thus, block sign generator 37 only needs to generate orthogonal codes at the block rate, not at the rate at which signal samples are generated or a "chip rate." The sign from block sign
20 generator 37 is combined with signal samples, such as bit b_3 from the block repetition unit 36 using an exclusive-OR or modulo-2 adder unit 38. Alternatively, a module-2 adder is used. A chip-rate scrambling code is produced from an access code generator 40 to randomize the output bit stream from the block sign adder 38. The code produced by the

between the despread symbols output by averager 58, which may be resolved by the exemplary maximum likelihood equalizer 60. The access code is preferably the same for all signals in the same cell while a different access code is employed for signals in different cells. The access code is preferably chosen according to the technique disclosed in U. S.

5 Patent No. 5,353,352 to achieve controlled non-orthogonality between cells.

In a third embodiment, the access code generator 40 is chosen to render multipath delayed signals orthogonal to non-delayed signals. This is achieved by applying like sign changes to any pair of adjacent chips in half of the block-repeats and unlike sign changes in the other half of the block-repeats. This has the effect that delays of \pm one chip relative to a nominal propagation delay result in multipath signals which are orthogonal to the nominal propagation path. The multipath signals are then not orthogonal but rather identically coded to another signal's code. This option is thus preferably used when only half the available codes are employed for discriminating between signals in the cell and the other half of the orthogonal codes are those which appear on \pm one chip-delayed multipath and thereby discriminate the multipath.

In a fourth embodiment, the access code generator 40 is a random code generator or none of the above. Then multipath signals are neither orthogonal to, nor identically coded with undelayed signals. If it is desired to demodulate multipath signals, then a RAKE type of equalizer may be employed, in which the receiver despreads the received signal using different time-shifted outputs of access code generator 52 and performs different averages for each using multiple instances of averager 58 to yield multiple averages each corresponding to signal ray of different propagation delay. The different rays are then combined in a RAKE equalizer such as the RAKE receiver using coarsely

quantized coefficients as described in the above-referenced U. S. Patent No. 5,305,349.

This fourth embodiment is preferably not suggested for application where degeneration of code orthogonality is affected by relative propagation delay differences or synchronization errors.

5 Advantageously, groups of signals that are not orthogonal, such as signals in different cells of a cellular wireless telephone system, are provided with different codes.

 The receiver 14 of Figs. 2, 3, and 4 is preferably constructed in accordance with the invention as shown in Fig. 7. Signals including desired signals, interfering signals, noise and multipath distortion signals are received from an antenna 44 and applied to an input 45
10 of a downconverter 46. The downconverter 46 downconverts the radio frequency signal to a signal suitable for processing, preferably a complex baseband signal. Complex baseband signals may be in Cartesian (X,Y) form having an X, or "I", real component and a Y, or "Q" imaginary component, or polar form (R,THETA) or Logpolar form (log(R), THETA) as described in U. S. Patent No. 5,048,059, issued to Dent on
15 September 10, 1991, entitled *Logpolar Signal Processing*, the disclosure of which is hereby incorporated by reference. The downconverted samples from output 47 of the downconverter 46 are then applied to a sign changer 48 which is connected to an access code generator 52. The downconverted samples 47 are then sign-changed by the adder 48 according to the sign pattern of an access code provided to the access code generator 52,
20 to remove the access code applied by a corresponding transmitter code generator such as access code generator 40 in Fig. 6. When different codes are applied to I and Q samples at the transmitter 16, Fig. 5, corresponding codes are used for I and Q samples, respectively, at the receiver 14, in Fig. 7.

equalized signals and produces hard decisions and "bad frame" indicators to source decoder 66. The source decoder 66 translates the output bitstream to, for example, speech signals and uses the bad frame indicators from the error decoder 64 to mask error events, and to prevent noise bursts from corrupting perceived speech quality. Further, a deinterleaver 62 is used between the equalizer 60 and the error correction decoder 64 if a corresponding interleaver is used at the transmitter 16. The deinterleaving by the deinterleaver 62 is not related to the use of the deinterleaver 56 to improve orthogonality under conditions of timing error or multipath.

Commonly assigned U. S. Patent Application No. 08/305,727 of Dent, entitled *Simultaneous Demodulation and Decoding Device* filed Sept. 14, 1994, discloses a decodulation technique which performs all the functions of the equalizer 60, the deinterleaver 62 and the error correction decoder 64 and may be used in lieu of these individual units. This disclosure is hereby incorporated by reference.

Small departures from true orthogonality that remain for some transmitted symbols when transmitters are not exactly synchronized are such as described by equation (1). For example, a joint demodulation method for two signals can proceed as follows:

If the signals b_N and a_2 are described as belonging to a vector $\underline{V}(i)$ of current symbols to be demodulated

$$\text{where } \underline{V}(i) = \begin{bmatrix} b_N \\ a_2 \end{bmatrix}$$

$$\text{and } \underline{V}(i+1) = \begin{bmatrix} b_{N'} \\ a_{2'} \end{bmatrix}$$

and $\underline{V}(i-1)$ is likewise composed of b_N and a_2 from previous blocks of N transmitted symbols, then upon combining repeats first with the sign pattern for 'b' symbols and then for 'a' symbols we obtain sums S_a and S_b as follows:

$$S_b = 4b_N - a_2 + a_2'$$

$$5 \quad S_a = 4a_2 - b_N + b_N'$$

or

$$\underline{S} = \begin{bmatrix} S_b \\ S_a \end{bmatrix} = \begin{bmatrix} 0 & 0 \\ 1 & 0 \end{bmatrix} \cdot \underline{V}(i-1) + \begin{bmatrix} .4 & -1 \\ -1 & 4 \end{bmatrix} \cdot \underline{V}_i + \begin{bmatrix} 0 & 1 \\ 0 & 0 \end{bmatrix} \cdot \underline{V}(i+1) \dots (4)$$

When all signals are to be demodulated, as in a cellular base station or satellite ground station, such remaining non orthogonality can be entirely compensated by joint demodulation, decision feedback, or alternatively the subtractive demodulation method of U. S. Patent 5,151,919 which was incorporated by reference herein above.

Thus, the sum vector S_b , S_a which should be $4\underline{V}_i$ is corrupted by a small amount of the previous vector $\underline{V}(i-1)$ and the next vector $\underline{V}(i+1)$, the amounts being described by "Intervector Interference" (IVI) coefficients which are matrices $M0$, $M1$ and $M2$ in the following equation:

$$\underline{S} = M0 \cdot \underline{V}(i-1) + M1 \cdot \underline{V}_i + M2 \cdot \underline{V}(i+1) \dots (5)$$

The center term is descrambled by multiplying equation (4) by the inverse of the matrix $M1$

$$20 \quad \begin{bmatrix} 4 & -1 \\ -1 & 4 \end{bmatrix}^{-1} = \begin{bmatrix} 4/15 & 1/15 \\ 1/15 & 4/15 \end{bmatrix}$$

to obtain

$$\underline{S}(i) = \begin{bmatrix} 1/15 & 0 \\ 4/15 & 0 \end{bmatrix} \cdot \underline{V}(i-1) + \underline{V}_i + \begin{bmatrix} 0 & 4/15 \\ 0 & 1/15 \end{bmatrix} \cdot \underline{V}(i+1) \dots\dots\dots (6)$$

which is equal to equation (4) multiplied by $M1^{-1}$.

The effect of the previous vector $\underline{V}(i-1)$ and the next vector $\underline{V}(i+1)$ may be approximately removed by using $\underline{S}(i-1)$ and $\underline{S}(i+1)$ computed using equation (6) and substituting them into equation (6) to obtain an improved estimate $\underline{S}(i)$ of $\underline{V}(i)$. This process is iterated to the extent needed to obtain the accuracy desired.

However, more generally, IVI expressed by equation (5) is unscrambled by use of a matrix transversal equalizer described by

$$\underline{S}(i) = \sum_{j=-L}^{j=+L} \left[H(j) \cdot \underline{V}(i+j) \right] \text{ where } L \text{ is selectively sized and the equalization matrices } H(j)$$

are chosen to obtain the desired accuracy of equalization.

It is not necessary to over complicate the process of compensating for residual non-orthogonalities when only a few of the N symbols per block are affected, particularly when the symbols are further processed by an error correction decoder. It may suffice to accord those symbols which are affected by residual non-orthogonality a soft value indicative of greater symbol uncertainty before applying them to the error correction decoder.

The present invention is capable of operating with any number of block repeats and not just the power of two for which Walsh-Hadamard sign patterns form orthogonal sets. This ability to generalize the invention relies on the fact that a radio signal is capable of being changed in phase by any desired amount and not just by inverting it 180 degrees. A

general phase shift of, for example, 120 degrees can be made and can be represented by multiplication by the complex factor:

$$S = \text{EXP}(j2\pi/3).$$

Presuming that a block of symbols is to be transmitted in three repeats in accordance with the invention, a first transmitter transmits its symbol blocks with successive phase shifts of 0, 120, and 240 degrees applied to the three block repeats. Using the symbols where S_0 , S_1 , S_2 , represent 0, 120 and 240, respectively.

$$S_0 = 1,$$

$$S_1 = \text{EXP}(j2\pi/3), \text{ and}$$

$$S_2 = \text{EXP}(j4\pi/3) = \text{EXP}(-j2\pi/3),$$

a first transmitter transmits $S_0.(b_1, b_2, b_3 \dots b_N)$; $S_1.(b_1, b_2, b_3 \dots b_N)$; $S_2.(b_1, b_2, b_3 \dots b_N)$;

where $(b_1, b_2, b_3 \dots b_N)$ stands for the block of symbols modulated without a phase shift.

A second transmitter transmits $S_0.(a_1, a_2 \dots a_N)$; $S_2.(a_1, a_2 \dots a_N)$; $S_1.(a_1, a_2 \dots a_N)$;

where $(a_1, a_2 \dots a_N)$ represents its modulated symbol block, and a third transmitter transmits

$S_0.(c_1, c_2 \dots c_N)$; $S_1.(c_1, c_2 \dots c_N)$; $S_2.(c_1, c_2 \dots c_N)$, where $(c_1, c_2 \dots c_N)$ is the third transmitter's modulated symbol block.

The three transmissions are orthogonal because the sequences

$$S_0, S_0, S_0, S_0, S_0, S_0, \dots;$$

$$S_0, S_1, S_2, S_0, S_1, S_2, \dots; \text{ and}$$

$$S_0, S_2, S_1, S_0, S_2, S_1, \dots;$$

are mutually orthogonal even when time-shifted. Such mutually orthogonal sequences of complex numbers may be called Fourier sequences and can be of any repeat length L of symbols by forming them as successive powers of $\text{EXP}(j2\pi/L)$.

The simpler, real-valued Walsh-Hadamard codes are used when the number of repeats L is a power of two.

In accordance with one aspect of the invention, other orthogonal sequences may also be constructed, for example by allowing a set of sequential multipliers for the successive repeats to be neither complex nor restricted to binary values of ± 1 . In particular, when the multipliers are chosen to be 1 or 0, the orthogonal sequences

1000000100000001000000...
0100000010000000100000...
0010000000100000001000...
0001000000010000000100...
0000100000001000000010...
0000010000000100000001...
0000001000000010000000...
0000000100000001000000...

arise, which in fact describe an 8-slot TDMA system in which each signal is transmitted in the slot in which a '1' occurs and not in which a '0' occurs. Thus, a TDMA system is reproduced as a special case of the of delay-insensitive orthogonal Code Division Multiple Access system of the present invention. Likewise when complex weights are selected from orthogonal Fourier sequences, when symbol blocks such as $(b_1, b_2, b_3, \dots, b_N)$ represent N -fold repeats of the same symbol 'b', and when each transmitter output signal is smoothed using a filter, the invention in this special case provides FDMA signaling in which different transmissions are mutually orthogonal independent of relative delay or mistiming by virtue of occupying different, unrelated frequency channels.

In accordance with another aspect of the invention, TDMA and FDMA systems can be reproduced as special systems and delay-insensitive orthogonal CDMA modes may be added to FDMA or TDMA systems by modification of their coding methods. Referring to Fig. 8(a), a prior art GSM TDMA signal burst and frame format consists of eight time slots, each of which contains a signal burst having components of a syncword surrounded by data bits. In standard GSM, the data bits in each of the eight timeslots belong to a different communications link or telephone call. Evolution of GSM to permit one link to use multiple timeslots provides higher user bit rates, in which case the data bits in successive slots can be from the same communications link or call.

Alternatively, Fig. 8(b) shows how, in accordance with the invention, the same data bits of Fig. 8(a) may be repeated successively with or without a phase inversion or phase change to form a delay-insensitive orthogonal CDMA signal. In Fig. 8(b), the positioning of each repeat preferably straddles two signal bursts, which advantageously avoids the guard time occurring between time slots and prevents a block from being split by a syncword. This has a positive effect on how well orthogonality is preserved under mistiming conditions, and also avoids the need to apply the orthogonal phase change sequence to the syncwords S. When a block straddles two time slots, the block is split by the guard time where zero energy is transmitted and not by the syncwords. This results in less reduction of orthogonality under mistiming as the zero energy symbols of the guard time cause less interference than the full energy symbols of the syncword, when they overlap data symbols.

Other arrangements of the repeats within the bursts can of course be used, and it is not necessary to have eight repeats. For example, using Fourier sequences, seven repeats

could be used with the eighth timeslot being used for receiving in the mobile terminal, to avoid a duplexing filter to connect the transmitter and receiver to the same antenna at the same time.

Those skilled in the art who now have the benefit of the present disclosure will appreciate that the present invention may take many forms and embodiments. Some embodiments have been presented so as to give an understanding of the invention. It is intended that these embodiments should be illustrative, and not limiting of the present invention. Rather, it is intended that the invention cover all modifications, equivalents and alternatives falling within the spirit and scope of the invention as defined by the appended claims.

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What is claimed is:

1. A communication system employing orthogonal block encoding, comprising:
a transmitter for repetitively transmitting encoded signals including mutually
orthogonally encoded repeated blocks of symbols, the symbols in the repeated blocks
representing successive samples of an informational source signal; and
5 a receiver for decoding the orthogonally encoded repeated blocks of symbols of
the encoded signals.
2. The communication system as recited in claim 1 wherein the receiver employs
different orthogonal codes to separate the encoded signals into corresponding separate
channels.
3. The communication system as recited in claim 1 comprising:
a memory for storing an orthogonal code; and
a phase shifter for responding to the stored orthogonal code for imposing a
corresponding sequence of phase changes onto at least one of the mutually repeated block.

14. The communication system as recited in claim 13 wherein

the blocks are transmitted at a preselected block repetition rate which results in a preselected repetition period; and

the adder comprises another adder for adding the corresponding symbols in
5 successive repeated blocks which are separated from each other by an amount equal to the repetition period.

15. The communication system as recited in claim 14 wherein each of the transmitters comprises:

an orthogonal encoder for selectively imposing phase shifts to the repeated blocks while being transmitted in accordance with an associated one of a plurality of the different
5 orthogonal codes; and

the receiver comprises a block remover for removing each of the repeated blocks received from each of the plurality of transmitters in accordance with the orthogonal code respectively associated with the transmitters being decoded before being applied to the adder.

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16. The communication system as recited in claim 13 wherein each of the transmitters comprises:

an orthogonal encoder for selectively imposing phase shifts to the repeated blocks while being transmitted in accordance with an associated one of a plurality of the different orthogonal codes; and

the receiver comprises a block remover for removing each of the repeated blocks received from each of the plurality of transmitters in accordance with the associated one of the plurality of different orthogonal codes being decoded before being applied to the adder.

17. The communication system as recited claim 13 wherein the receiver comprises an equalizer for processing the summed signal from the adder to compensate for multipath propagation effects.

18. The communication system as recited in claim 9 wherein each of the plurality of transmitters comprises a digital source encoder for producing the symbols as digital bits of information.

19. The communication system as recited in claim 18 wherein each of the plurality of transmitters comprises:

a repeater for repeating each bit of information produced by the digital source encoder a preselected number of times to successively produce groups of repeated bits;

5 a sign changer for selectively imposing a sign change on the repeated bits of each of a number of successive groups of repeated bits equal to the preselected number of bits in each group in accordance with an orthogonal code associated with the transmitter;

an interleaver for interleaving the sign changed bits from the preselected number of groups to successively generate a number of blocks each composed of the different sign
10 changed bits of the preselected number of repeated groups and having a collective sign change corresponding to a common sign change shared by all signed changed bits of the block; and

a sign modulator for transmitting a signal modulated in accordance with the generated blocks with sign changes corresponding to the orthogonal code.

20. The communication system as recited in claim 19 wherein each of the transmitters comprises an error correction encoder for imposing error correction encoding upon each of the digital bits of information from the digital source encoder.

21. The communication system as recited in claim 19 wherein each of the transmitters comprises:

an access code generator for generating access code sequences at the rate at which digital bits of information are produced by the digital source encoder; and

5 an access code imposer for imposing the access code onto the individual digital bits of each of the orthogonally encoded blocks.

22. The communication system as recited in claim 21 wherein the receiver comprises an access code decoder for decoding the individual digital bits of information received at the receiver.

23. The communication system as recited in claim 19 wherein the decoder comprises a deinterleaver for separating the blocks into individual digital bits.

24. The communication system as recited in claim 23 comprising a maximum likelihood equalizer for equalizing the individual digital bits from the deinterleaver.

25. The communication system as recited in claim 23 wherein the receiver comprises an error correction decoder.

30. The method as recited in claim 29 comprising the step of transmitting the blocks at a preselected rate and adding symbols in successive repeated blocks.

31. The method as recited in claim 29 wherein the steps of removing the orthogonal coding comprising the steps of:

forming a summed signal for each symbol within the repeated blocks; and
processing the summed signal with an equalizer.

32. The method system as recited in claim 26 comprising the step of encoding the symbols as digital bits of information.

33. The method as recited in claim 32 comprising the steps of:

repeating each bit of information produced by the digital source encoder a preselected number of times to successively produce groups of repeated bits;

imposing a sign change on the repeated bits of each of a number of successive groups of repeated bits equal to the preselected number of bits in each group in accordance with an orthogonal code;

interleaving the sign changed bits from the preselected number of groups to successively generate a number of repeated groups and having a collective sign change corresponding to a common sign change shared by all signed changed bits of the block;
and

transmitting a signal modulated in accordance with the generated blocks with sign changes corresponding to the orthogonal code.

34. In a communication system having a plurality of transmitters and a receiver for communicating with the transmitters, a method for orthogonal block encoding, comprising the steps of:

repetitively transmitting from each of the plurality of transmitters encoded signals
5 with mutually orthogonal encoded repeated blocks of symbols respectively representing successive samples of an informational source signal produced at the transmitter; and
decoding the orthogonally encoded repeated blocks of the transmitted encoded signals received from all the plurality of transmitters at the receiver by employing different ones of a plurality of orthogonal codes respectively associated with different ones of the
10 plurality of transmitters for separating the received encoded signals into corresponding channels.

35. The method as recited in claim 34 in which the step of transmitting includes the steps of:

storing at each transmitter a different one of a plurality of orthogonal codes at each
of the plurality of transmitters; and
5 imposing a sequence of phase changes onto each repeated block in accordance with the stored orthogonal code.

36. The method as recited in claim 35 wherein the step of imposing a sequence comprises the step of selecting a 180 degree phase shift for imposing onto the repeated blocks in accordance with the stored orthogonal code.

37. The method as recited in claim 36 wherein the step of storing comprises the step of:

selecting a Walsh-Hadamard code having a number of bits equal to the number of times the transmitters repeatedly transmit the repeated blocks.

38. The method as recited in claim 34 wherein the step of decoding comprises the steps of:

removing the orthogonal encoding from the encoded transmitted signals; and

adding corresponding ones of the symbols in successively received repeated blocks

5 from one of the plurality of transmitters after the orthogonal encoding is removed by the encoder to form a summed signal for each symbol within the repeated blocks.

39. The method as recited in claim 38 comprising the steps of:

transmitting the blocks by a preselected block repetition rate which results in a preselected repetition period ; and

adding the corresponding symbols in successive repeated blocks which are

5 separated from each other by an amount equal to the repetition period.

44. The method as recited in claim 43 comprising the steps of:

repeating each bit of information produced a preselected number of times to
successively produce groups of repeated bits; and

selectively imposing a sign change on the repeated bits of each of a number of
5 successive groups of repeated bits equal to the preselected number of bits in each group in
accordance with an orthogonal code associated with the transmitter;

interleaving the signed changed bits from the preselected number of groups to
successively generate a number of blocks each composed of the different sign changed bits
of the selected number of repeated groups and having a collective sign change
10 corresponding to a common sign change shared by all signed changed bits of the block;
and

transmitting a signal modulated in accordance with the generated blocks with sign
changes corresponding to the orthogonal code.

45. The method as recited in claim 44 comprising the step of imposing error correction
encoding upon each of the digital bits of information.

46. The method as recited in claim 44 comprising the steps of:

generating access code sequences at the rate at which digital bits of information
are produced by the digital source encoder; and

imposing the access code onto the individual digital bits of each of the
5 orthogonally encoded blocks.

47. The method as recited in claim 46 comprising the step of decoding the individual digital bits of information received at the receiver.

48. The method as recited in claim 44 comprising the step of separating the blocks into individual digital bits.

49. The method as recited in claim 48 comprising the step of equalizing the individual digital bits with a maximum likelihood equalizer.

50. The method as recited in claim 49 comprising the step of including an error correction decoding for the receiver.

51. A method of transmitting a spread-spectrum encoded signal with improved tolerance of multipath propagation, comprising the steps of:

encoding information to produce symbol blocks containing a predetermined first number of symbols;

5 repeating transmission of each symbol block a selected number of times; and
changing a sign of each successively repeated block according to a preselected
sequence of sign changes.

52. The method as recited in claim 51 in which the spread-spectrum coded signal is further conditioned prior to transmission by combining with a spread-spectrum access code.

53. The method as recited in claim 52 in which the spread-spectrum access code is used in common by different transmitters.

54. The method as recited in claim 51 in which the selected sequence of sign or phase changes is different for a different transmitter.

55. The method as recited in claim 54 in which the different sequences are orthogonal to one another.

56. A method for decoding a spread-spectrum coded signal, comprising the steps of:
receiving a composite signal, the composite signal being a sum of a number of overlapping spread-spectrum signals including said coded signal and sampling the composite signal to produce signal samples;

5 combining selected ones of the signal samples separated by a predetermined number of samples using a phase change selected from a preassigned pattern of phase changes associated with a particular one of the overlapping spread-spectrum signals to produce despread samples; and

10 processing the despread samples using an equalizer to compensate for multipath propagation.

57. A transmitter for use in a communication system employing orthogonal block encoding, the transmitter comprising:

a producer of an information source signal;

a transmitter circuit for repetitively transmitting encoded signals comprised of
5 mutually orthogonally encoded repeated blocks of symbols respectively representing samples of the informational source signal;

a memory for storing an orthogonal code; and

a phase shifter responsive to the stored orthogonal code for imposing a corresponding sequence of phase changes into each repeated block.

58. The transmitter as recited in claim 57 comprising a digital source encoder for producing the symbols as digital bits of information.

59. The transmitter as recited in claim 58 comprising:

a repeater for repeating each bit of information produced by the digital source encoder a preselected number of times to successively produce groups of repeated bits;

5 a sign changer for selectively imposing a sign change on the repeated bits of each of a number of successive groups of repeated bits equal to the preselected number of bits in each group in accordance with an orthogonal code associated with the transmitter;

10 an interleaver for interleaving the sign changed bits from the preselected number of groups to successively generate a number of blocks each composed of the different sign changed bits of the preselected number of repeated groups and having a collective sign change corresponding to a common sign change shared by all signed changed bits of the block; and

a sign modulator for transmitting a signal modulated in accordance with the generated blocks with sign changes corresponding to the orthogonal code.

60. The transmitter as recited in claim 59 comprising an error correction encoder for imposing error correction encoding upon each of the digital bits of information from the digital source encoder.

61. A receiver for use in processing encoded transmitted signals having mutually orthogonally encoded repeated blocks of symbols the receiver comprising a receiver circuit for receiving the encoded transmitted signals, and for decoding the orthogonally encoded repeated blocks of symbols of the transmitted encoded signals received from the transmitters, by employing different ones of a plurality of orthogonal codes respectively associated with different ones of a plurality of orthogonal codes associated with different ones of the transmitters to separate the received encoded signals into corresponding separate channels.

62. The receiver as recited in claim 61 comprising:
an orthogonal code remover for removing the orthogonal encoding from the encoded transmitted signals; and
an adder for adding corresponding ones of the symbols in successively received repeated blocks after the orthogonal encoding is removed by the orthogonal code remover to form a summed signal for each symbol within the repeated blocks.

63. The receiver as recited in claim 62 wherein the blocks are transmitted at a preselected block repetition rate which results in a preselected repetition period; and
the adder comprises another adder for adding the corresponding symbols in successive repeated blocks which are separated from each other by an amount equal to the repetition period.

64. The receiver as recited in claim 63 wherein the receiver comprises a block remover for removing each of the repeated blocks received from the transmitter in accordance with the orthogonal code associated with the transmitter before being applied to the adder.

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Abstract of the Disclosure

A communication system and method with orthogonal block encoding is provided.

5 Encoded signals are transmitted by repeating transmissions of symbol blocks with a phase or sign change selected for each block from a sequence of phase or sign changes. Different symbols are transmitted using orthogonal sequences. The decoding uses different orthogonal sequences for separating the received encoded signals into corresponding separate channels. The orthogonal encoding is removed from the encoded transmitted
10 signals and corresponding ones of the repeated symbols are added in successively received repeated blocks after the orthogonal encoding is removed. A transmitter uses a digital source encoder to encode information into symbols, and each symbol is repeated a preselected number of times to successively produce groups of repeated bits. Each repeat bit is changed in phase or sign by application of a sign or phase change determined by a
15 selected assigned orthogonal code associated with the transmitter. The sign changed bits are interleaved from a number of such groups to successively generate a number of blocks, each composed of the different sign or phase changed bits of the preselected number of repeated groups and having a collective sign or phase change corresponding to a common sign change or phase shared by all bits of the block. The interleaved blocks then modulate
20 a radio signal for transmission.

Fig.1

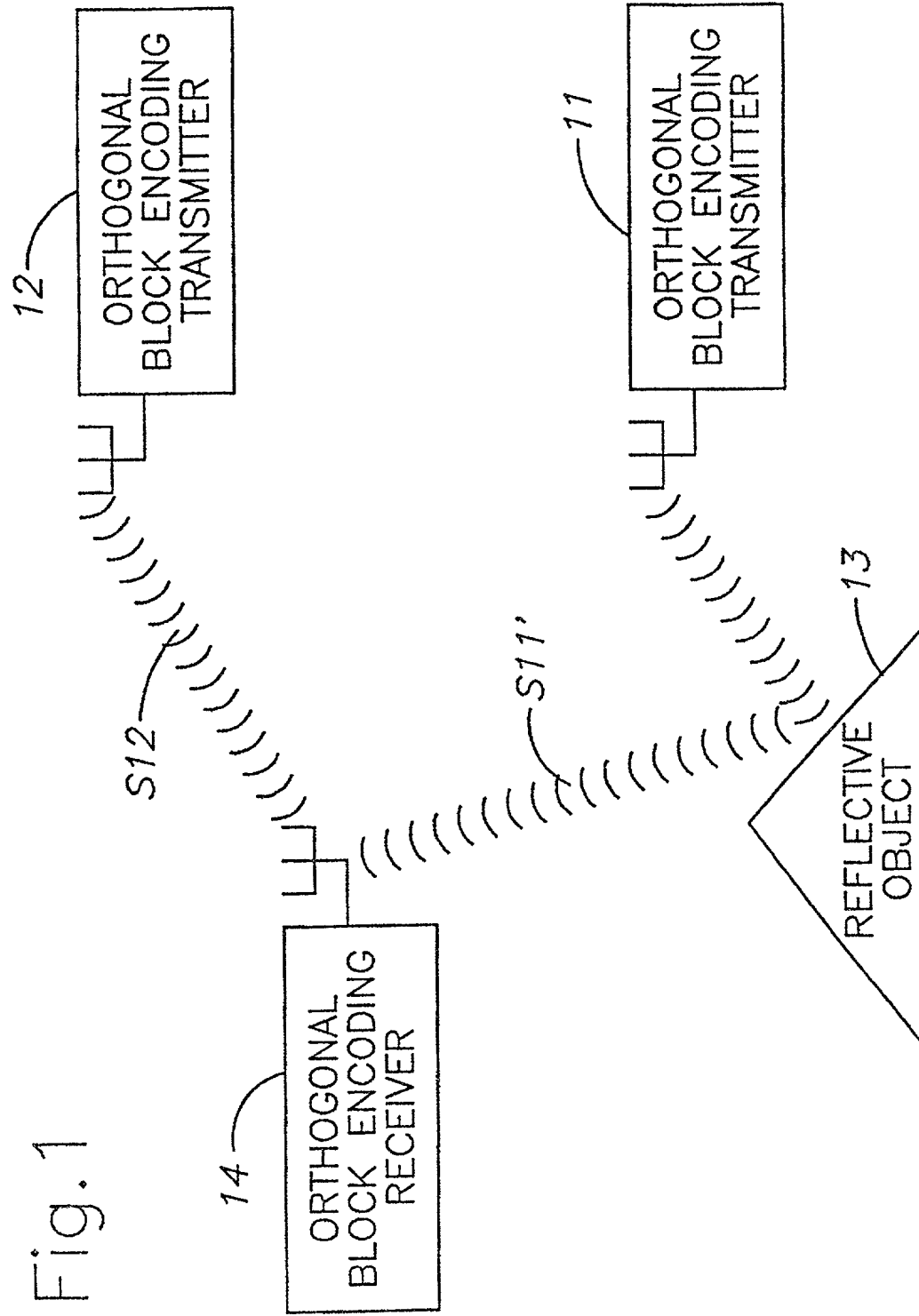


Fig.2

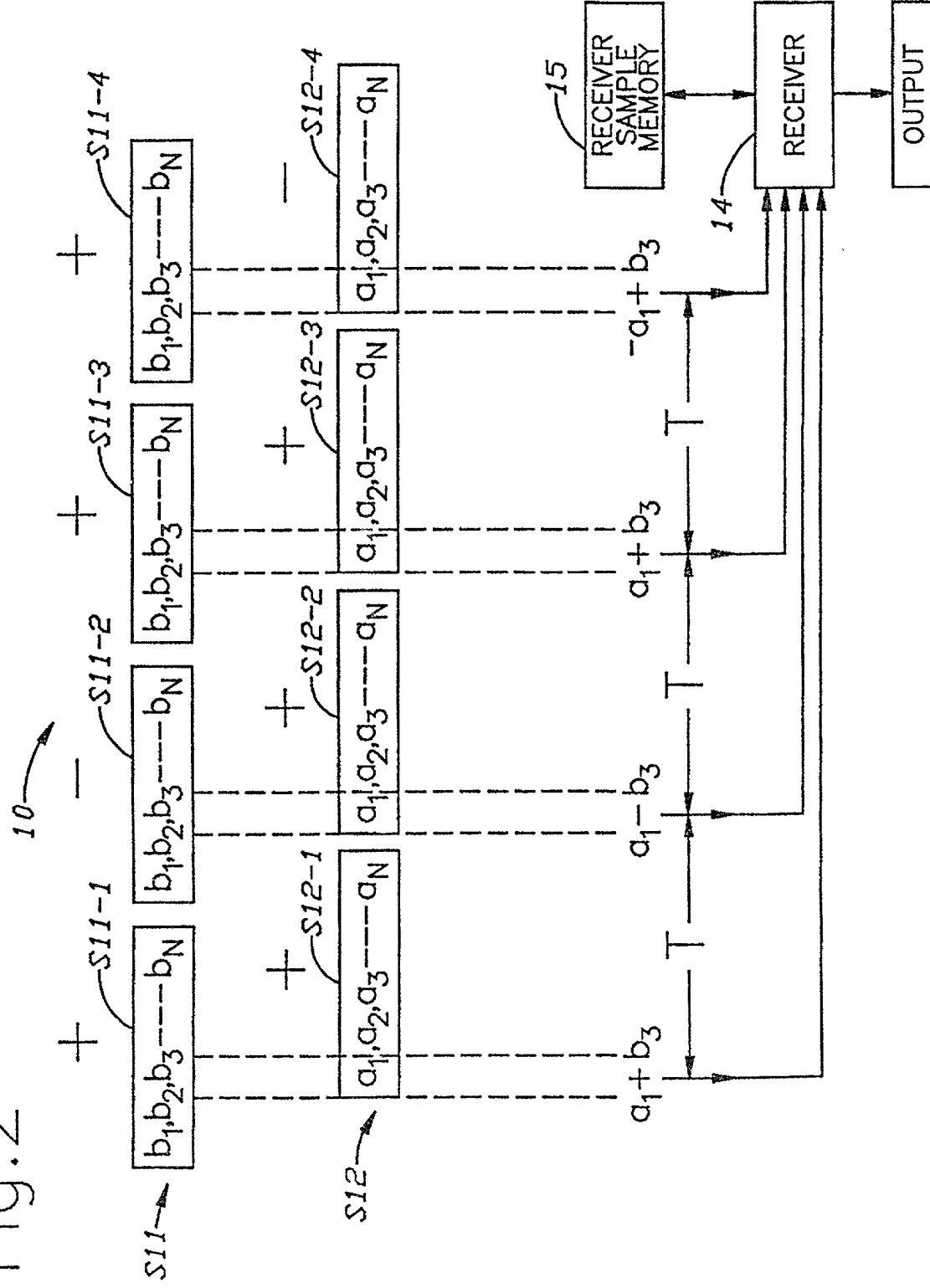
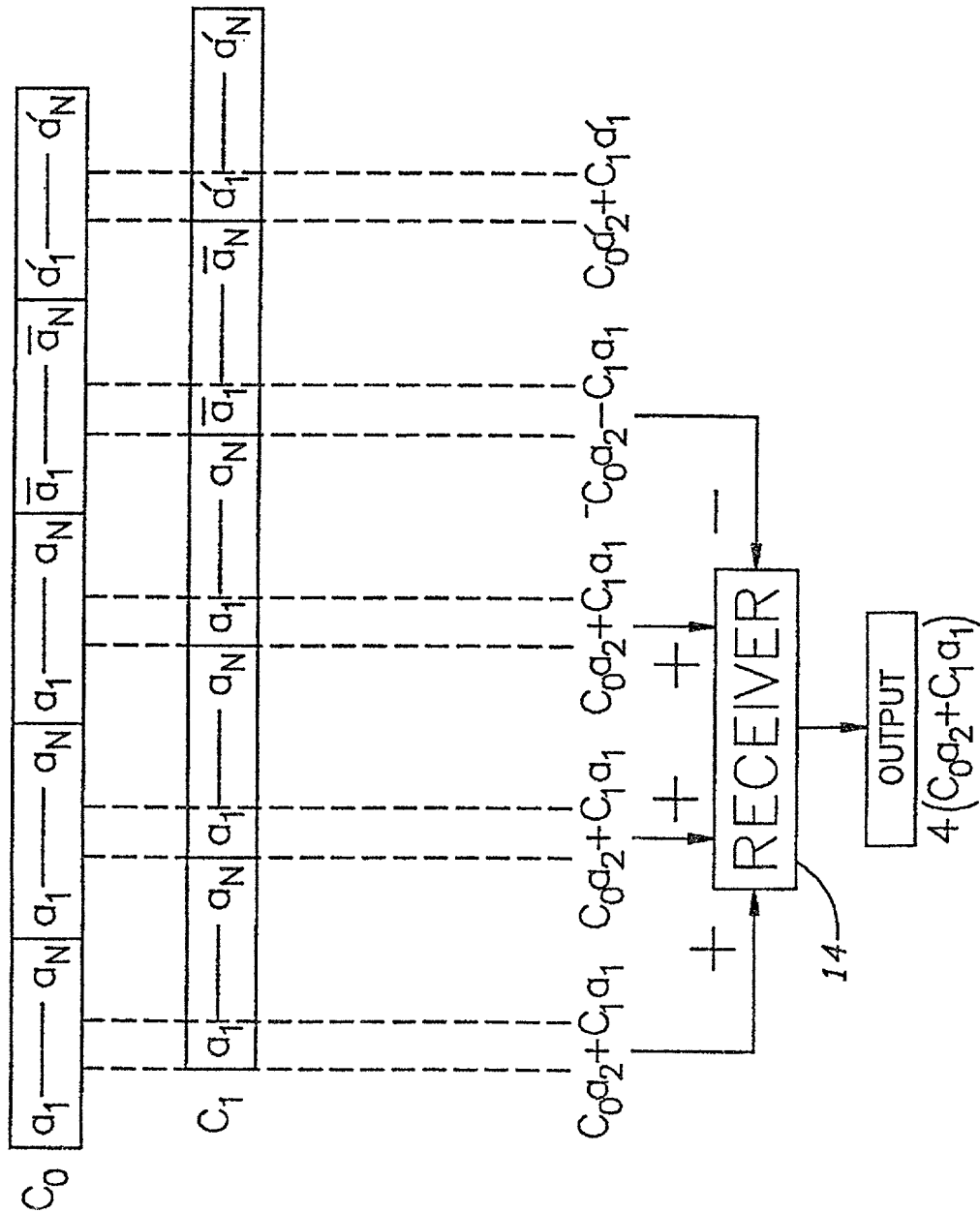


Fig. 4



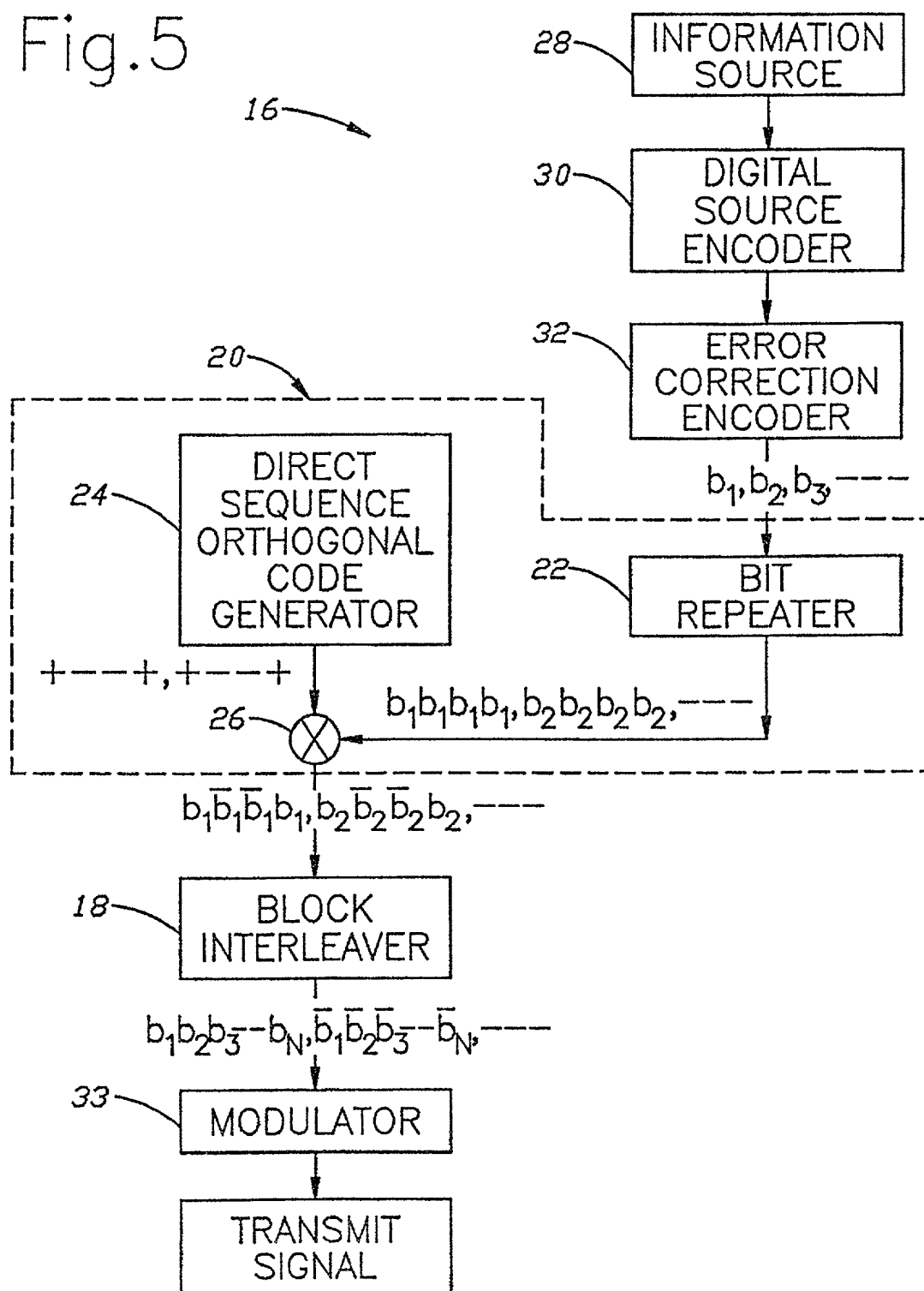


Fig. 6

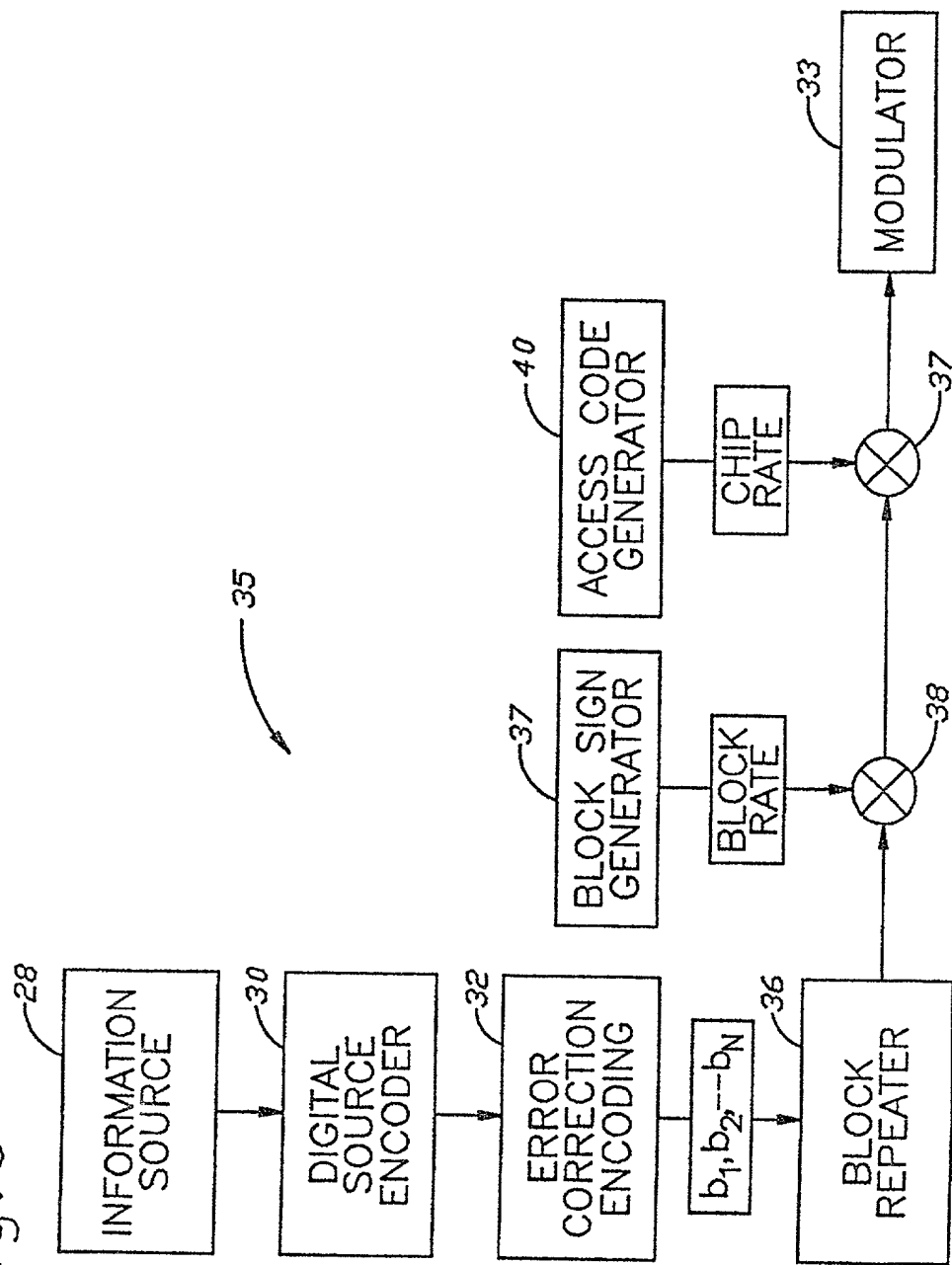


Fig. 7

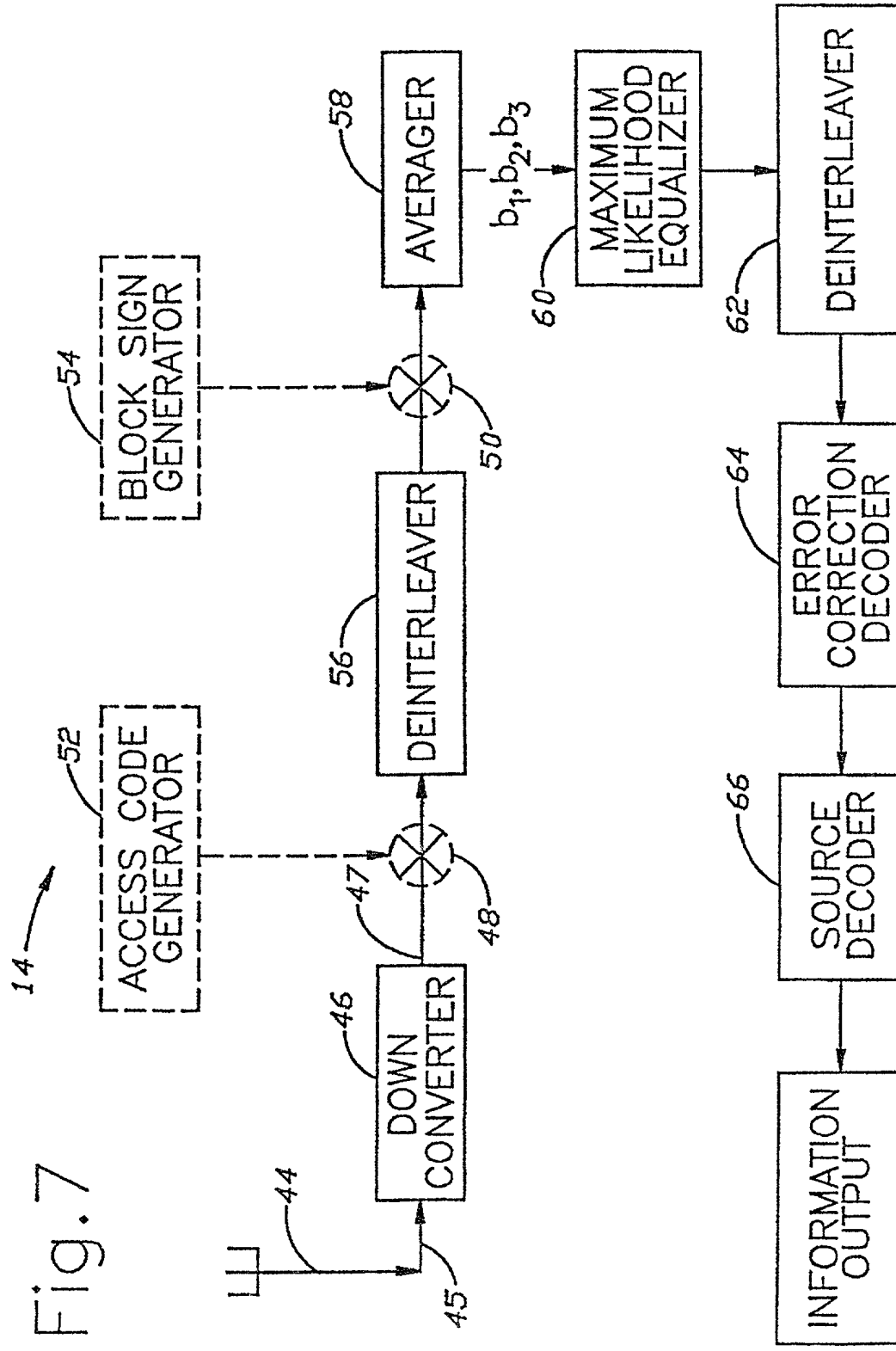


Fig.8A

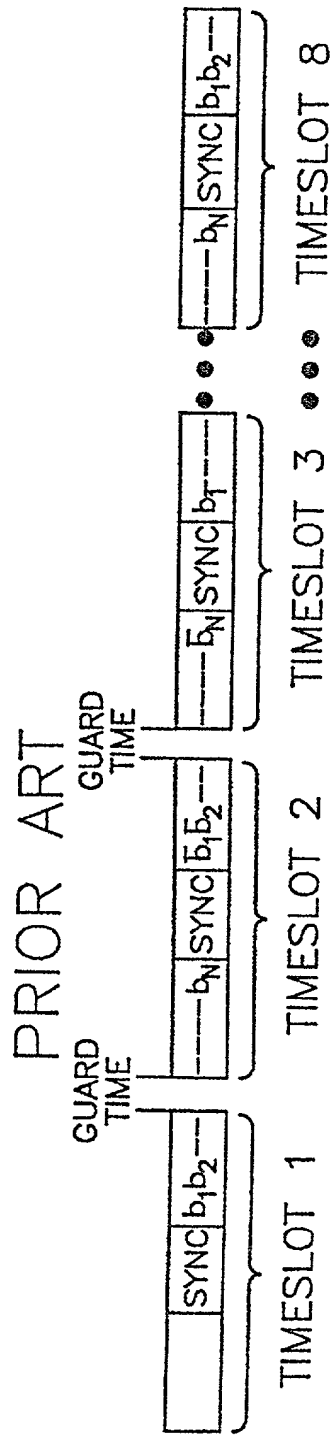
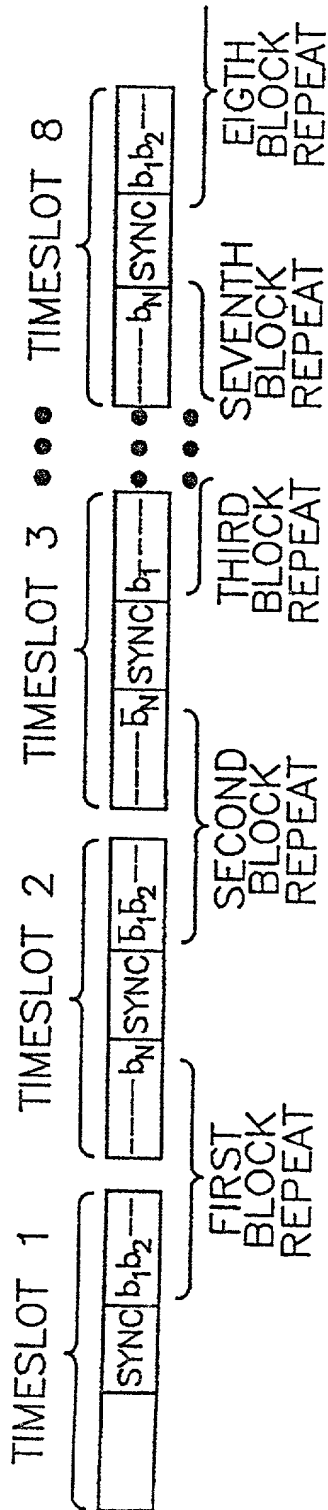


Fig.8B



Attorney Docket Number
EUS00872-RCUR

Declaration and Power of Attorney for Patent Application

As a below named inventor, I hereby declare that:

My residence, post office address and citizenship are as stated below next to my name,

I believe I am the original, first and sole inventor of the subject matter which is claimed and for which a patent is sought on the invention entitled **COMMUNICATION SYSTEM AND METHOD WITH ORTHOGONAL BLOCK ENCODING**, the specification of which

☒ is attached hereto.

(check one)

☐ was filed on _____ as
Application Serial Number _____
and was amended on _____
(if applicable)

I hereby state that I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment referred to above.

I acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56(a).

I hereby claim foreign priority benefits under Title 35, United States Code, § 119 of any foreign application(s) for patent or inventor's certificate listed below and have also identified below any foreign application for patent or inventor's certificate having a filing date before that of the application on which priority is claimed:

Prior Foreign Application(s)

Priority Claimed

(NONE) _____
(Number) (Country) (Day/Month/Year Filed)

☐ ☐
YES NO

(Number) (Country) (Day/Month/Year Filed)

☐ ☐
YES NO

(Number) (Country) (Day/Month/Year Filed)

☐ ☐
YES NO

Attorney Docket Number
EUS00872-RCUR

Declaration and Power of Attorney for Patent Application

I hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and, insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56(a) which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

(NONE)

(Application Serial No.)

(Filing Date)

(Status: Patented/Pending/Abandoned)

(Application Serial No.)

(Filing Date)

(Status: Patented/Pending/Abandoned)

Power of Attorney: As a named inventor, I hereby appoint the following agents/attorneys to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

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Attorney Docket Number
EUS00872-RCUR**Declaration and Power of Attorney for Patent Application**

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Full name: Paul W. Dent
First Name Middle Name/Initial Last Name

Signature: Paul W. Dent Date: 97-7-17
First Name Middle Name Last Name Year-Month-Day

Residence: 637 Eagle Point Road, Pittsboro, North Carolina 27312
City, State, and Country

Citizenship: British

Post Office Address: _____

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Paul Wilkinson Dent	Examiner:	Boakye, A.
Filing Date:	22 July 1997	Attorney Docket:	P08283-US1
Serial No.:	08/898,392	Art Unit:	2733
For: Communication System and Method with Orthogonal Block Encoding			

Assistant Commissioner for Patents
Washington, D.C. 20231

ASSOCIATE POWER OF ATTORNEY

Sir:

The undersigned attorney of record in this case hereby appoints the below names person as associate attorney to prosecute this patent application and to transact all Business in the United States Patent and Trademark Office therewith and with the resulting patent:

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John R. Owen, Reg. No. 42,055; Benjamin S. Withrow, Reg. No.
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Please direct all telephone calls about this application and the resulting patent to:

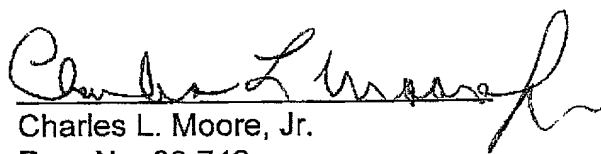
David E. Bennett
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The above-appointed associate attorney is hereby authorized to act and rely on instructions from and communicate directly with the assignee of record and with such patent agent/attorney liaison individual or firm, selected by the assignee, as may represent themselves to the associate attorney as having been so appointed, unless and until I instruct the associate attorney in writing to the contrary.

Respectfully Submitted,

June 23, 1999


Charles L. Moore, Jr.
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